

REMARKS

Reconsideration of this application as amended is respectfully requested. No new matter has been added. Claim 149 has been canceled without prejudice. Claims 120-150 remain pending. The remarks below regarding claim rejections refer to the claims as amended herein.

Claim Rejections - 35 U.S.C. § 102

Claim 142 has been rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,970,457 to Richards et al (“Richards”). Applicant respectfully disagrees with this reason for rejection.

Claim 142 recites, in part:

a CAM array having compare lines coupled to the CRC
circuit to receive at least a portion of the CRC value
therefrom

Richards discloses a data transmission apparatus comprising a CAM coupled to receive, over an internal bus (Richards, Figure 5, element 33), ATM cell header information from a control logic (Richards, Figure 5, element 6), and to return the number of the entry in the CAM where a match occurs (Richards, col. 6 lines 48-55). Richards further discloses a CRC engine (Richards, Figure 5, element 7) to generate a CRC value, which is implemented by a DMA engine to update a running CRC stored in the context memory (Richards, col. 11 lines 12-15 and 41-43). Thus, as further illustrated in Figure 6, Richards discloses that the CAM receives ATM cell header information, not the CRC value generated from the CRC engine. Richards clearly does not disclose “a CAM array having compare lines coupled to the CRC circuit to receive at least a portion of the CRC value” as recited in claim 142 and therefore does not anticipate claim 142, nor dependent claims 143-145.

Claims 146-149 have been rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,934,795 to Nataraj et al (“Nataraj”). Claim 149 has been canceled. Applicant respectfully submits that Nataraj does not anticipate claim 146, nor dependent claims 147 and 148.

Claim 146 recites, in part:

a multiplexer to output a predetermined priority value in an operation to determine where to store a first value within the CAM, and to output a selected priority value from the priority index table in an operation to determine whether a specified value is stored within the CAM.

Nataraj discloses a multiplexer (Figure 19, element 1929) that outputs either a 5-bit or a 10-bit priority number from the priority index banks PA1-PA8 in response to a select signal which corresponds to a specific word width configuration of the CAM array (Nataraj, col. 37 lines 30-62). Nataraj does not disclose or suggest a multiplexer to “output a predetermined priority value in an operation to determine where to store a first value within the CAM, and to output a selected priority value from the priority index table in an operation to determine whether a specified value is stored within the CAM”, as recited in claim 146. In view of this clear distinction, Nataraj does not anticipate claim 146, nor dependent claims 147 and 148.

Allowable Subject Matter

Applicant acknowledges that claims 130-141 and 150 are allowed.

In Conclusion

Applicant respectfully submits that all pending claims are in condition for allowance. If a telephone interview would be helpful in any way, the examiner is invited to call the undersigned attorney.

A petition for a 1-month extension of time is enclosed herewith.

Authorization is hereby given to charge deposit account 501914 for any fee deficiency associated with this Amendment.

Respectfully submitted,

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